1-Gbit Network Search Engine



INT-11000

1-Gbit Network Search Engine Controller

Product Brief, features and benefits summary

Highly customizable hardware IP block. Easily portable to Faraday-Tech's structured ASIC flow, Xilinx or Altera FPGAs

INT-11000 is highly flexible that is customizable for layer-3 through Layer-7 network security and network infrastructure applications. It is recommended for use in, among others, high performance Network security appliances and Network infrastructure appliances. It provides the key IP building block for a high performance G-bit Network content search ASIC/SOC/ASSP/FPGA solutions.

INT-11000 provides capability for enterprises to differentiate their Network security appliances from others

INT-11000 can search contents in 1.4M packets, in-line in both directions, simultaneously, at full G-bit rate. The Direct write of search results to Memory interface relieves the host CPU from costly management execution and maintenance tasks.

- Full functionality proven on ASIC, SOC, Xilinx SOC-FPGA and Systems validated in IDS/IPS appliances:
- Gate count less than 50K gates
- Organization width of 64k -1024k deep tables
- Generic 'Host side bus' interface
- Programmable Host side interface 32/64/128 bit
- Variable FIFO depth from 16-256 Bytes at the Host side interface
- Interfaces to TCAMs up to 72 M bits
- Search width 36 576 bits wide
- Max. Search time = 8 clks
- Fast, intelligent information retrieval.

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1-Gbit Network Search Engine

- Allows Cascading of multiple CAMs
- Customizable host control interface for pattern programming
- Up to 250 MHz full-featured performance
- Widest search widths (up to 576 bits) available today.
- Status and control can be managed via Host interface
- Fully integrated SSRAM controller (optional)

Deliverables:

Netlist, Verilog Source code, models

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